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10/18/00 PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Art Unit 2812

Terry L. Gilton and Li Li

Application No.: 09/321,518

Filed: May 27, 1999

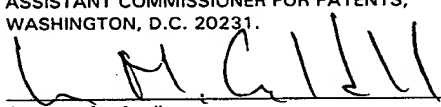
For: SEMICONDUCTOR FABRICATION  
METHODS AND APPARATUS

Examiner: --

Date: August 18, 1999

CERTIFICATE OF MAILING

I hereby certify that this paper and the documents referred to as being attached or enclosed herewith are being deposited with the United States Postal Service on August 18, 1999 as First Class Mail in an envelope addressed to: ASSISTANT COMMISSIONER FOR PATENTS, WASHINGTON, D.C. 20231.

  
Attorney for Applicant

U.S. PTO  
09/579345



**INFORMATION DISCLOSURE STATEMENT**  
**PURSUANT TO 37 C.F.R. § 1.97(b)**

TO THE ASSISTANT COMMISSIONER FOR PATENTS  
Washington, DC 20231

Sir:


Listed on the accompanying form PTO-1449 and enclosed herewith are several English-language documents. Applicants respectfully request that these documents be listed as references cited on the issued patent.

This Information Disclosure Statement was filed before applicants received a first Office action on the merits. However, if a first Office action on the merits was mailed before this Information Disclosure Statement was filed, please charge any fees that may be required in connection with the filing of this Information Disclosure Statement, or credit overpayment, to Deposit Account No. 02-4550. A duplicate copy of this Information Disclosure Statement is enclosed.

Respectfully submitted,

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